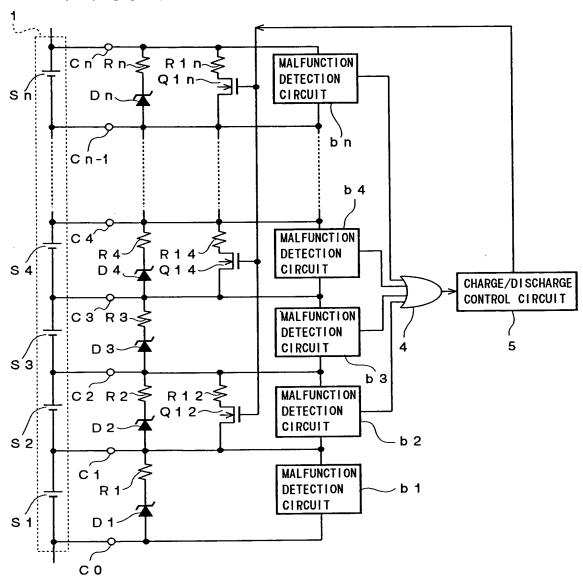
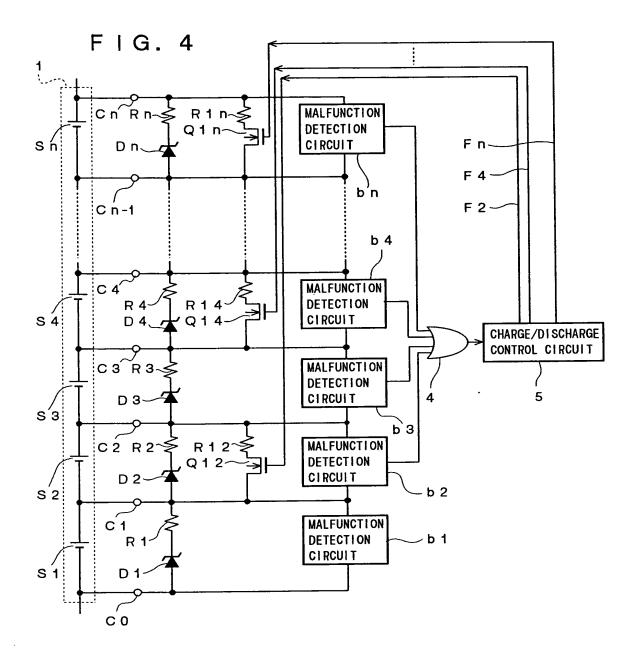
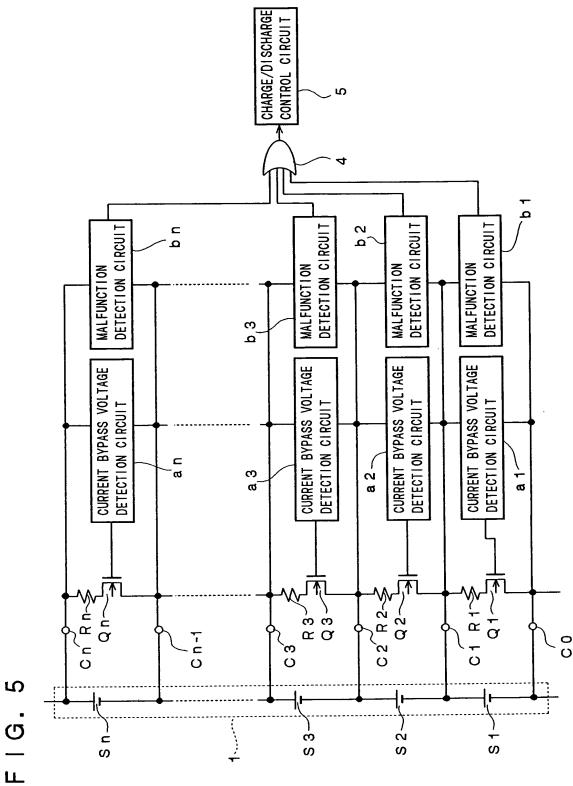


F | G. 3







F | G. 6 MALFUNCTION Cn Rn-DETECTION S'n Dn. CIRCUIT b'n C n -1 b 4 MALFUNCTION DETECTION C 4 R 4 CIRCUIT S 4 D 4 CHARGE/DISCHARGE CONTROL CIRCUIT C 3 R 3 MALFUNCTION DETECTION D 3-CIRCUIT 5 s 3 C2 R2 MALFUNCTION DETECTION D 2-CIRCUIT - b 2 S 2 C 1 MALFUNCTION R 1 DETECTION / b 1 CIRCUIT S 1 D 1

сo